Ecology Treatment of Printed Circuit Boards

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Abstract: This contribution contents one possibility of PCB separation due to the temperature due to the different thermal expansion of the metal path and the plastic from which the boards are made. The separation of metal and plastic may occur due to temperature changes if the temperature difference is sufficient. We have carried out a study of printed circuit board production so we can choose the appropriate path of separation. Furthermore, we determined the shear stress size required to tear the conductive copper paths from the epoxy resin. The temperature field in a two-layer board was modelled in simulation software FEMLAB and Pro/ENGINEER. From the simulated temperature field simulations, conclusions can be drawn that accurately describe the condition and characteristics of materials subjected to heat shock. In our own experiments, we used several ways to heat PCBs. After using the mechanical separation of the components, they were dropped from the PCB. Mechanical separation was also used when removing conductive paths. This separation is effective, but in the newer types of PCBs, the cyclical effects of thermal shock have to be applied to the separation of copper paths. Experimental tests have demonstrated the viability of the proposed method. The proposed method of recycling could lead to industrial use in case consistent sorting of waste electrical and electronic parts of old items.

Keywords: Shear stress, thermal shock, stress, linear expansion, electronic waste, printed circuit board (PCB), recycling, conductive paths, Pro/ENGINEER, COMSOL Multiphysics.

1. Introduction

In recent years with the development of consumer electrics the recycling of electric waste has become a necessity due to the lowering prices of electrics we experience its massive consumption as well as decrease of its lifetime. Retired electrics normally used to end in the solid and hazardous waste landfills. Because these products contain many valuable components, but also materials that are harmful to the environment, we have focused our work on the possibilities of reuse of the retired electric waste, originating mainly from computing technologies, as a source of valuable raw materials.

Electric waste is currently the fastest growing type of the waste. Globally it now constitute up to 5% of weight of the solid domestic waste which is almost as much as plastic packaging. In the European Union countries, where households annually produce about 8 million tons of electric waste, the electric waste is growing at a rate of 3-5% per year, almost three times faster than the total waste volume. Developing countries even assume that their electric waste production will triple by 2010. Experts estimate that in line with the growth in sales of electrics the European Union will have to cope with almost 11 million tons of the electric waste just in 2020. Ten years later the annual production will rise to almost 14 mil. tons. We must add to this another approximately three million tons of the electric waste originating from organizations and companies [1, 2, 4].

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2. Linear expansion of PCB materials

The following figure shows the status of materials during the temperature change. It is evident that the expansion of the material 1 is much smaller than in the material 2. This is due to the different coefficients of thermal expansion of both materials. We used this property to derive a formula for the calculation of the resulting shear stress which is needed for separation of the conductive paths and plastic materials from the PCB [4].

\[ \sigma = E \alpha \Delta T \]

Resultant force \( F_T \) can be specified

\[ F_T = (\sigma_1 - \sigma_2) S \]

where \( F_T \) - a resultant force [N], \( \sigma_1 \) - tensile stress of the first material [Pa], \( \sigma_2 \) - tensile stress of the second material [Pa], \( S \) - the contact surface of both materials [m²].

This force must be greater than the allowable shear stress for the materials, which gives the following relationship:

\[ \frac{F_T}{S} > \tau \]

where \( \tau \) - the maximum allowable shear stress of the material [MPa].

For the calculation we selected the combination of epoxy resin and copper [7].

Calculation shows the size of the force in dependence on change of the temperature which is required to peel the copper surface paths from the epoxy resin. The required shear stress for separation of Cu path from PCB is 109°C.

Table 1: Mechanical properties of materials [7].

<table>
<thead>
<tr>
<th></th>
<th>FR4</th>
<th>Copper</th>
</tr>
</thead>
<tbody>
<tr>
<td>( E ) (Pa)</td>
<td>22·10⁹</td>
<td>11·10⁹</td>
</tr>
<tr>
<td>( \alpha ) (K⁻¹)</td>
<td>18·10⁻⁶</td>
<td>17·10⁻⁶</td>
</tr>
</tbody>
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3. Mathematic Model of Non-stationary Temperature Field of Two Layers Folder

For describing heating and cooling process of two layers folder we used following mathematic model [5]:

\[ \frac{\partial t_1}{\partial \tau} (x, \tau) = \frac{\partial^2 t_1}{\partial x^2} (x, \tau), \quad \tau > 0, 0 < x < b \]

\[ \frac{\partial t_2}{\partial \tau} (x, \tau) = \frac{\partial^2 t_2}{\partial x^2} (x, \tau), \quad \tau > 0, b < x < \infty \]

\[ t_1 (x, 0) = t_2 (x, 0) = t_p \]

\[ t_1 (b, \tau) = t_2 (b, \tau) \]

\[ t_1 (0, \tau) = t_o \]

\[ \lambda_1 \frac{\partial t_1}{\partial x} (b) = \lambda_2 \frac{\partial t_2}{\partial x} (-b) \]

After solving we obtained an accordance [6, 11]
\[ t_1 - t_p = \frac{t_0}{1 + K_e} \sum_{n=1}^{\infty} \frac{h^{n-1}}{(K_e)^n} \left( \frac{x - b + (2n-1)\sqrt{K_e} b}{2\sqrt{a_1 t}} \right) \]  

\[ \frac{t_1 - t_p}{t_0} = \text{erfc} \left( \frac{x}{2\sqrt{a_1 t}} \right) - \frac{b}{t_0} \sum_{n=1}^{\infty} h^{n-1} \left[ \text{erfc} \left( \frac{2n-x}{2\sqrt{a_1 t}} \right) - \text{erfc} \left( \frac{2nb+x}{2\sqrt{a_1 t}} \right) \right] \]

where \( h = \frac{1-K_e}{1+K_e} \)  

\[ K_e = \frac{a_1}{a_2} \quad \text{and} \quad K_e = \sqrt{\frac{\lambda_1 c_{p1}\rho_1}{\lambda_2 c_{p2}\rho_2}} \]

where \( a \) - thermal diffusivity, \( m^2 \cdot s^{-1} \); \( c_p \) - specific heat capacity, \( J \cdot kg^{-1} \cdot K^{-1} \); \( t \) - temperature of the heated (cooled) body, \( ^\circ C \); \( t_0 \) - ambient temperature, \( ^\circ C \); \( b \) - thickness, \( m \); \( \lambda \) - thermal conductivity, \( W \cdot m^{-1} \cdot K^{-1} \); \( \rho \) - density, \( kg \cdot m^{-3} \); \( t \) - time, \( s \); \( s \) - properties of the first layer, \( 2 \) - properties of the second layer.

### 3.1 Thermal stress of multilayer walls composed of isotropic layers

The influence of temperature changes on the inhomogeneous, layered plates/shells are generally manifested in two ways. If there is any (even homogeneous) body statically stored indefinitely, preventing the imposition of free thermal expansion of the body, creating a corresponding strain. For multilayer walls thermal stress occurs as a result of the inhomogeneity, when the thermal expansion in equidistant surfaces of walls into the generally different thermal expansion coefficients in the layers.

Consider the generally layer wall, for example the wall with generally variable properties after thick, exposed thermal effects of any temperature profile \( \Delta T(x) \), corresponding to a certain point transient of heat transmission of the wall.

Not only the elastic constants, but the coefficient of linear thermal expansion is generally varies discontinuously at the interface of both layers as well as continuously within each of the layers, due to any dependence of these parameters on the temperature \([9]\).
Thermal stress $\sigma^T_x = \sigma^T_y$ in the area of the ordinate $y$ will be:

$$\sigma^T(x) = E^T(x)\left[\varepsilon^T - \alpha(x) \Delta T(x)\right].$$

Equation (9) defines stress in the equidistant planes with the plate of plane, except as normal and interlaminar shear stresses which representing the local stress concentrations at the edges of the layers.

3.2 FEM modeling of transient thermal stress

Diagrams of shear stresses are plotted in figure 4. Unlike normal interlaminar component the sign peak shear stress does not play any role for subsequent separation of the layers. How to heating and a subsequent rapid cooling the layered structure to cause formation of significant stress peaks, which can cause separation of the layers, applying in the recycling process.

Based on the results of the modeling of transient thermal stress of PCBs is possible to comment on potential uses of these stress for the separation of the Cu layers in the process of recycling computer waste. The chosen procedure tempering and rapid cooling you can rate depending on the parameters (temperature, residence time) in terms of the level interlinear stress and thus its effectiveness.

4. Experimental Section

Hot air gun has been use for the most effective heating to 370°C. The temperature was measured contactlessly using radiometer Thermopoint 64/64 plus, having a range from -30 to 900°C. The temperature is sufficient even for the separation of a tin. After using mechanical separation components dropped away from the PCB. We used mechanical separation and when you delete the conductive paths. Laboratory tests showed that the temperature difference must be much greater than the calculation gives. It is due to the fact, that copper is very plastic and the calculated expansion caused by the temperature difference is smaller than expected. For newer types of PCB it was necessary to inject more thermal shocks for the separation of the copper paths. The proposed method of recycling could lead to industrial use, which requires prior diligent sorting of the electric waste.

5. Results and Discussion

In our own experiments we have used several methods of heating of the PCB. Heating PCB at a press tempered at 200°C, further microwave heating. Was the most effective heating with a heat gun to 350°C. This temperature is sufficient even for the separation of tin, because the melting point of tin is 250°C. After using mechanical separation, parts dropped away from the PCB. We used a mechanical separation when you delete the conductive paths. Laboratory tests showed that the temperature difference must be much greater than the calculation gives. It is due to the fact, that copper is very plastic and the calculated expansion caused by the temperature difference is smaller than expected. For newer types of PCB it was necessary to inject more thermal shocks for the separation of the copper paths. The proposed method of recycling could lead to industrial use, which requires prior diligent sorting of the electric waste.

6. Conclusions

The results show that this path of thermal shock separation is feasible because, according to the experiments performed, it was due to different lengths of metal and plastic expansion to tear the copper conductive paths from the PCB. Because PCBs are unfortunately not only double layers but also multilayered, I am waiting for a mathematical model for a multilayer PCB and a proposal for separation. This environmental method of PCD separation could be of great importance in industrial practice.

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References and Notes


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